

Fig. 1.A

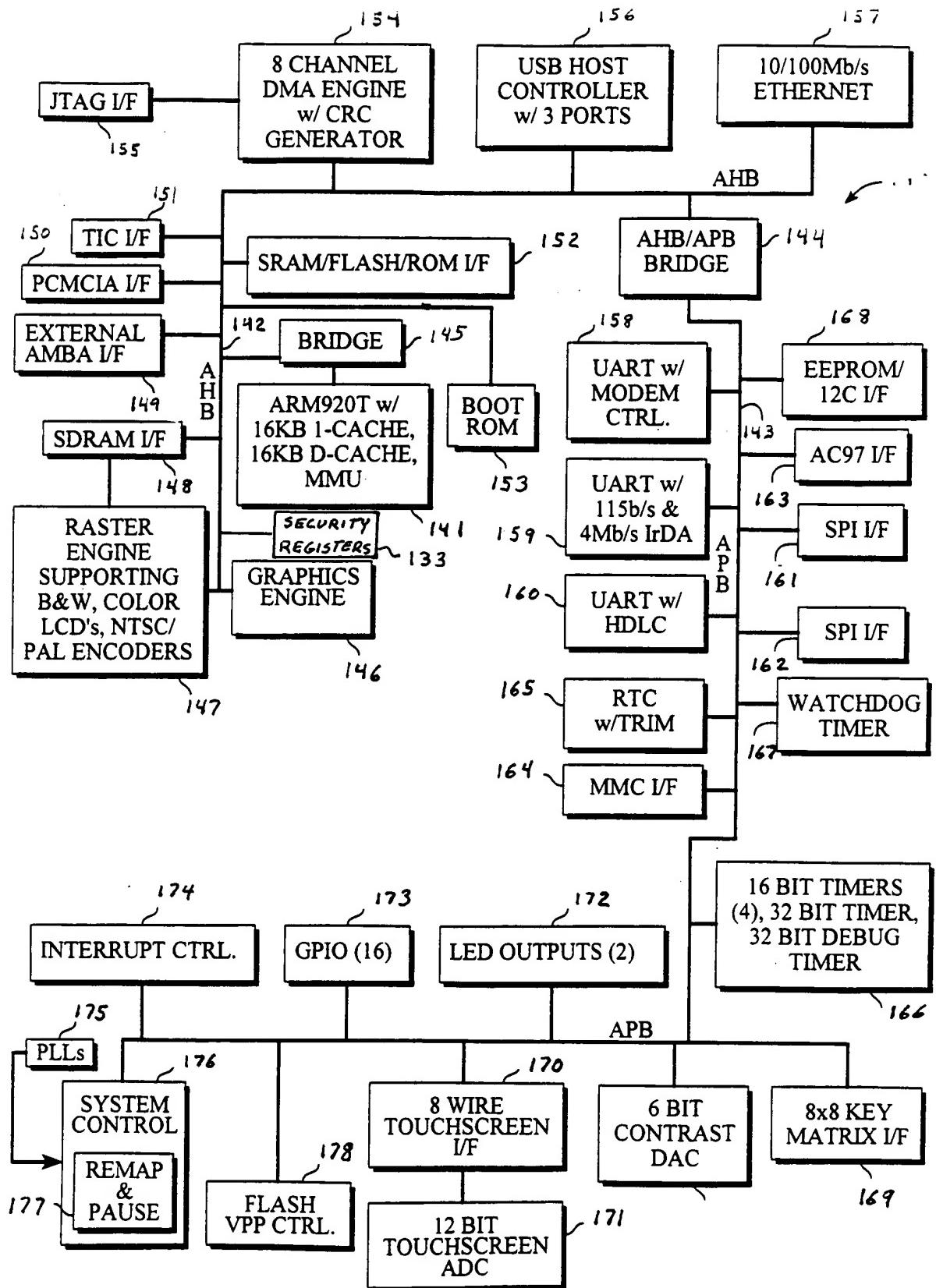


Fig. 1B

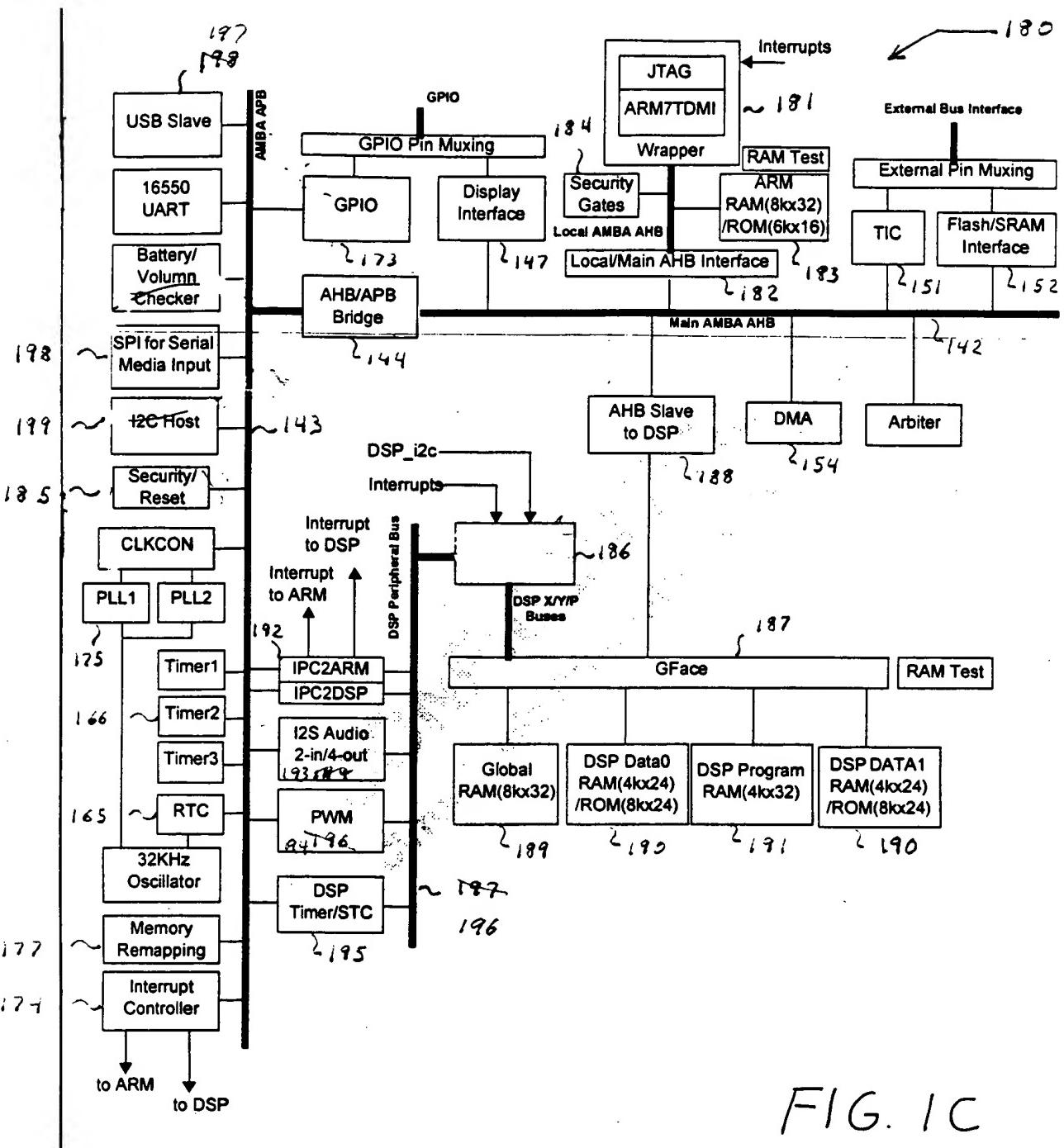


FIG. 1C

FIG.

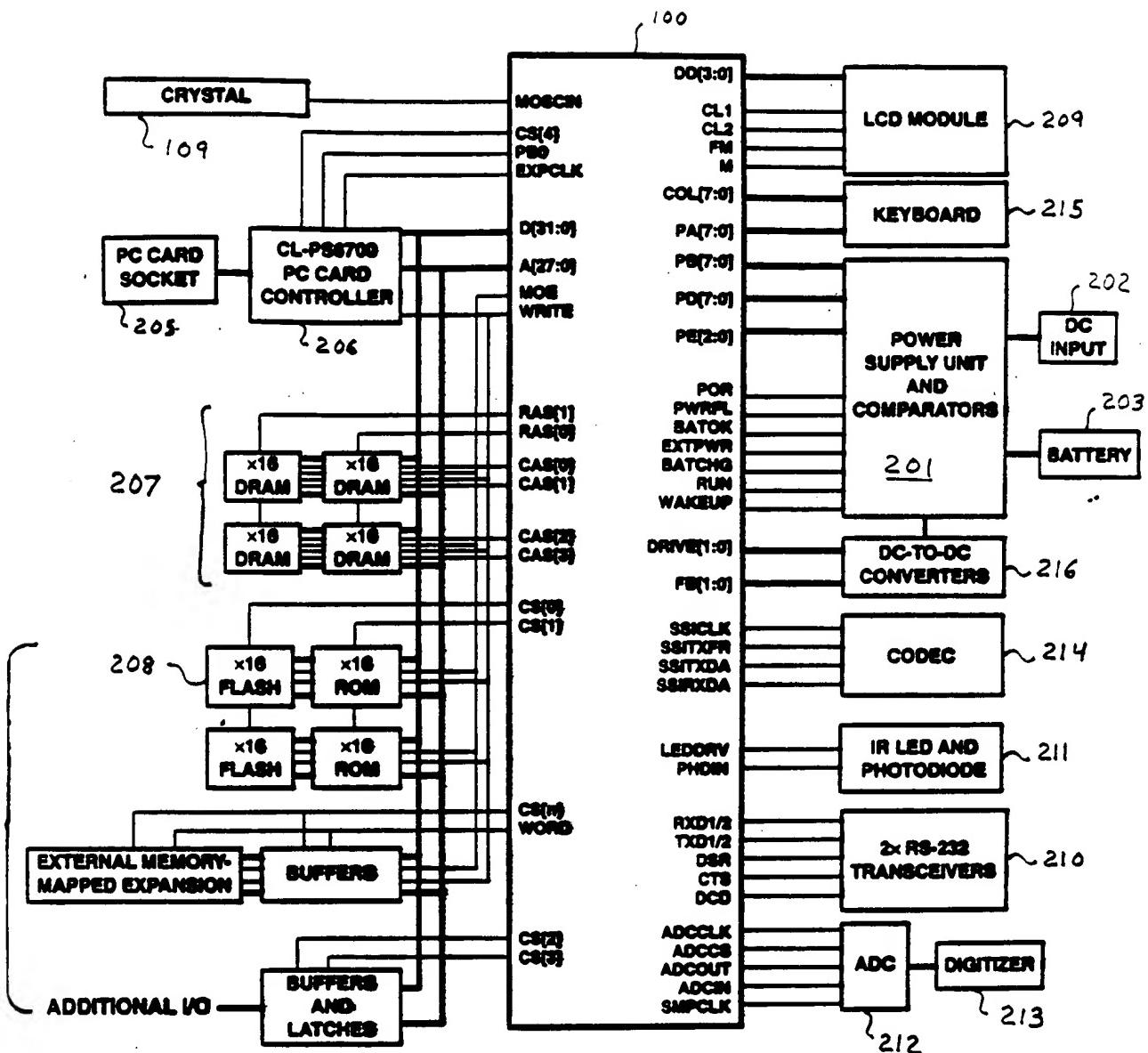
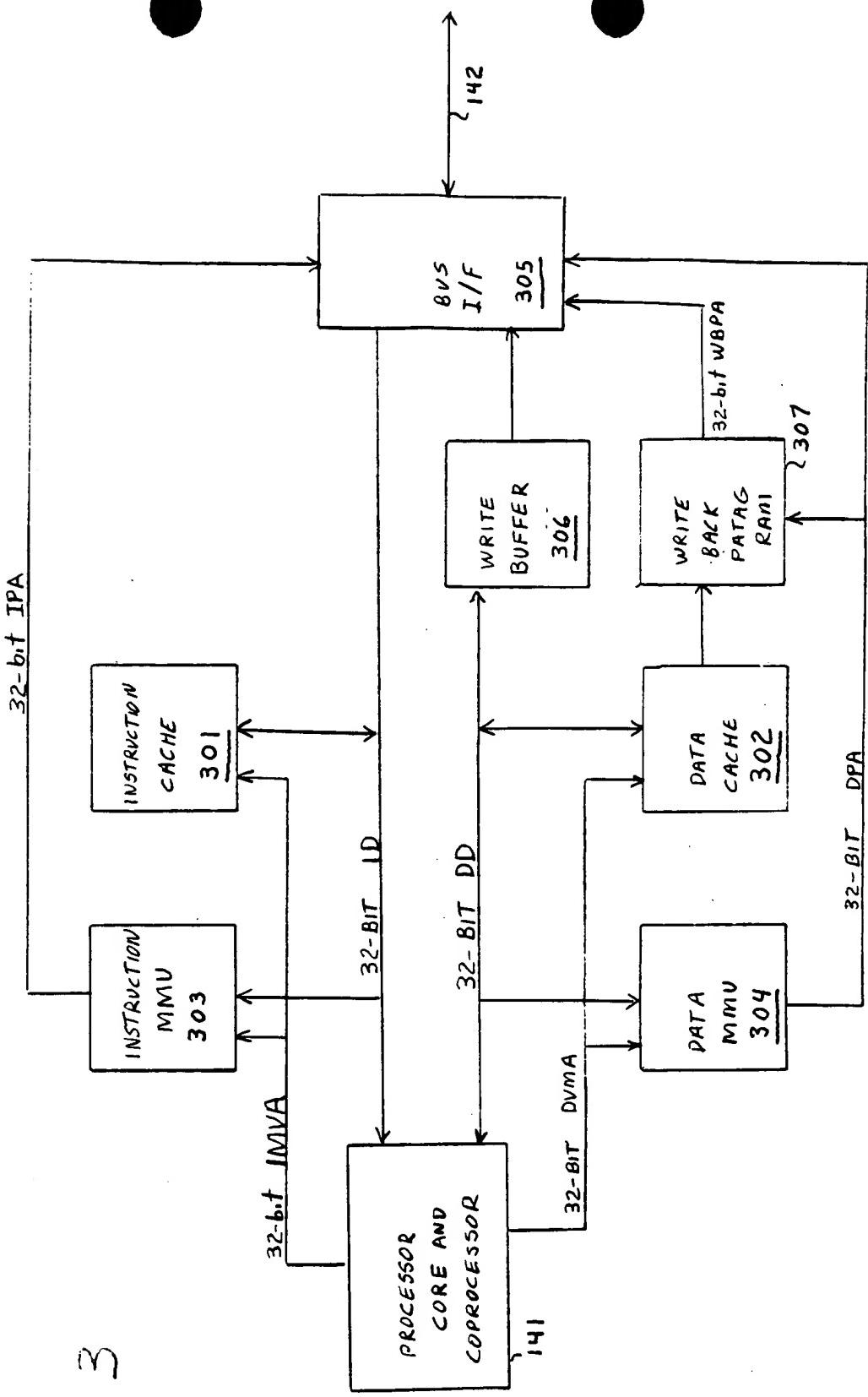


FIG. 3



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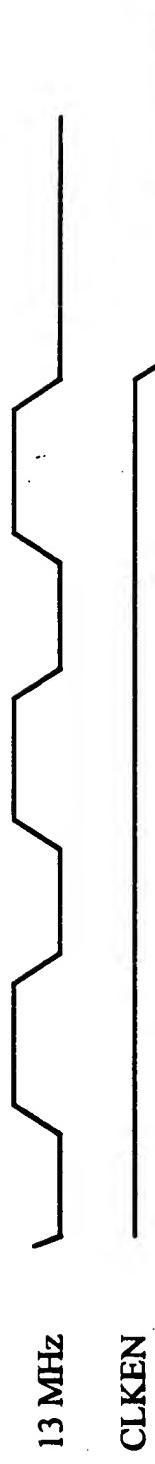


Fig. 4A

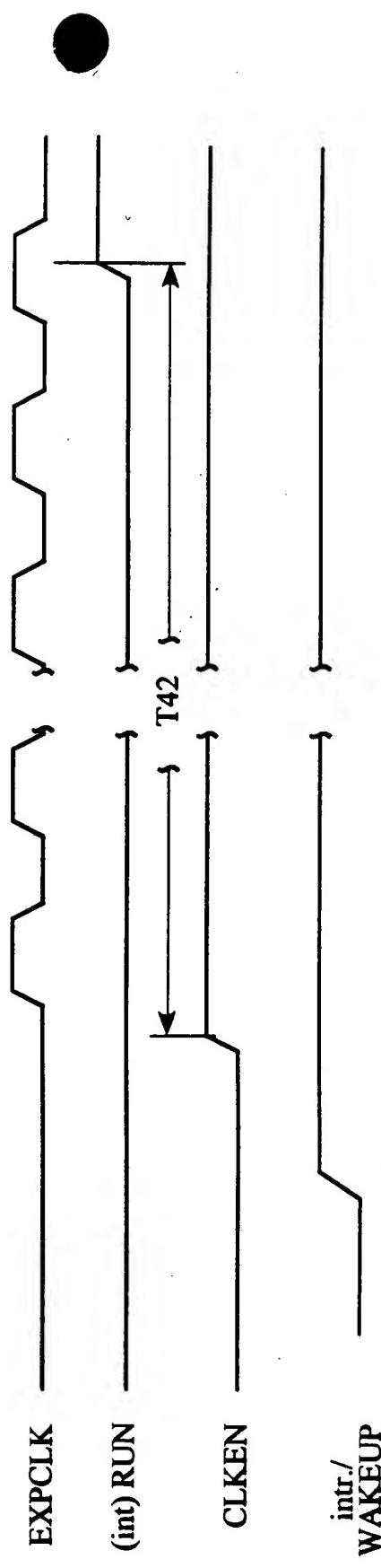


Fig. 4B

Fig. 5

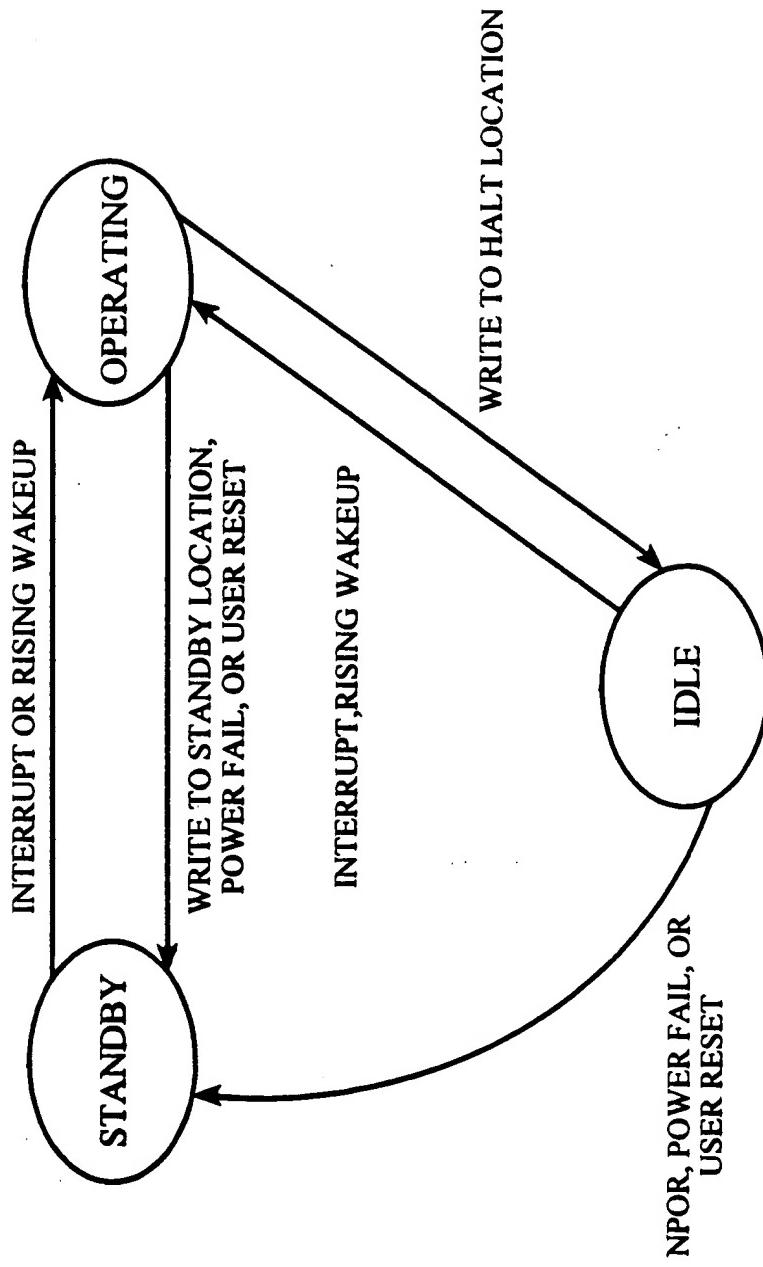
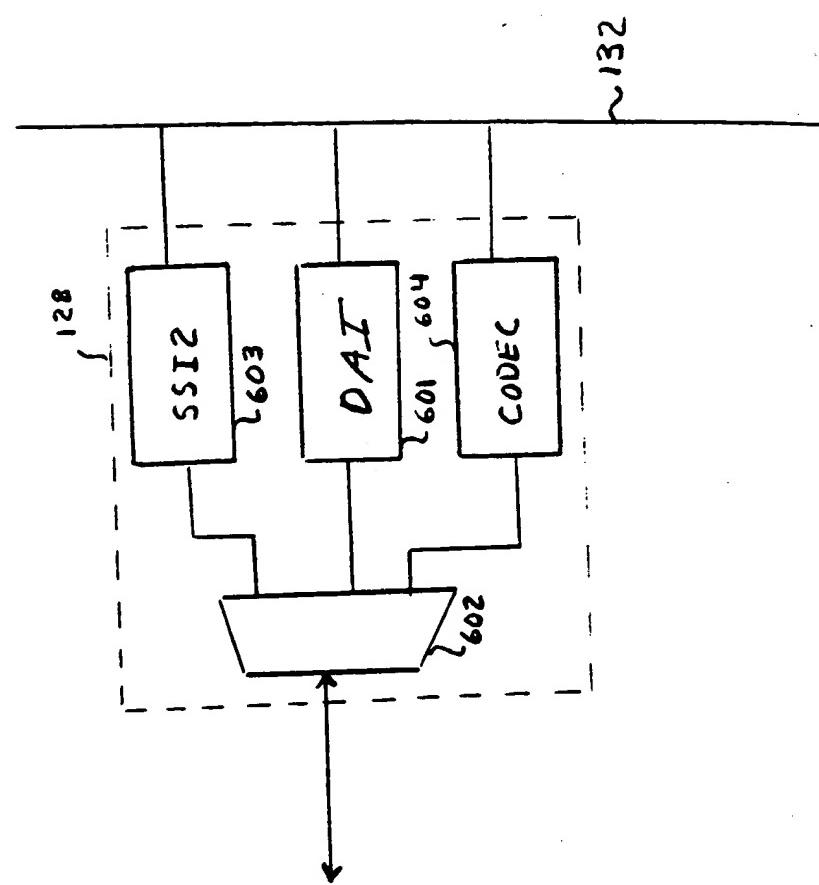
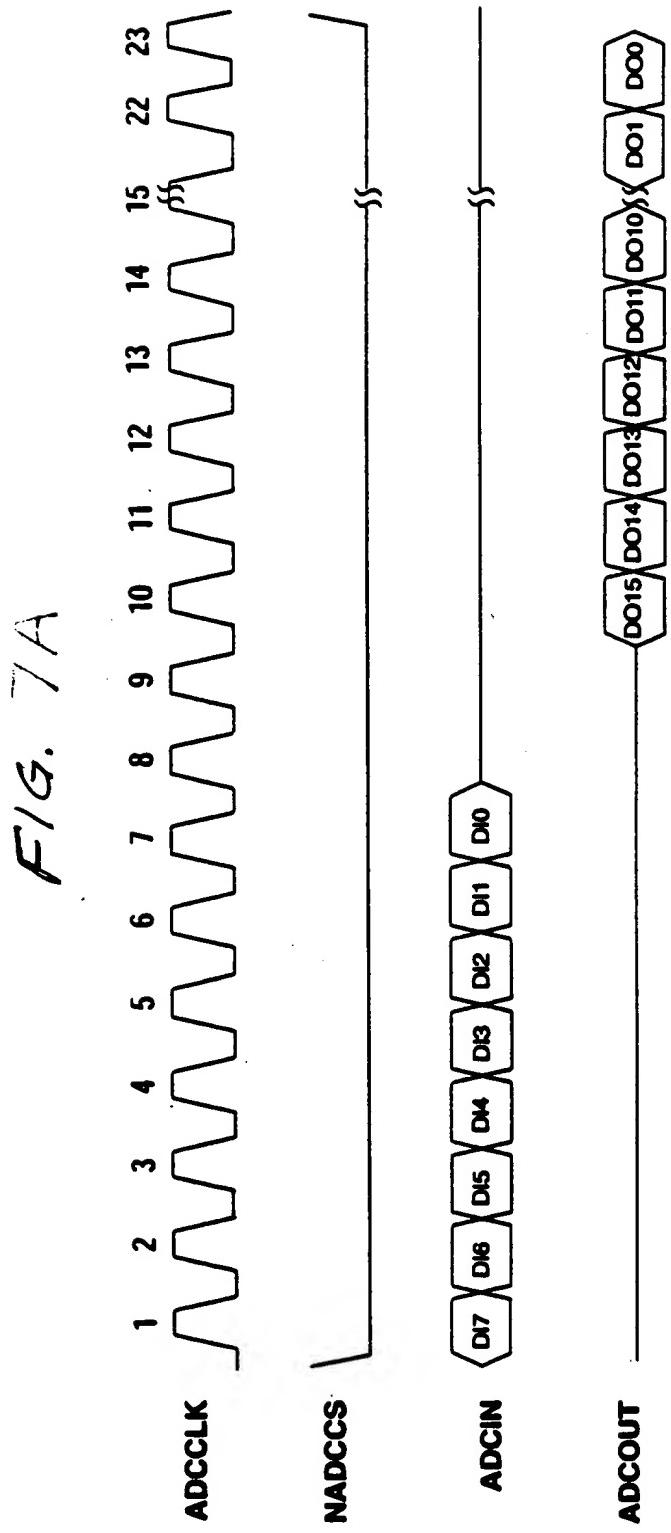


FIG. 6



SSI Timing Interface for MAX148/9



SSI Interface for AD7811/2

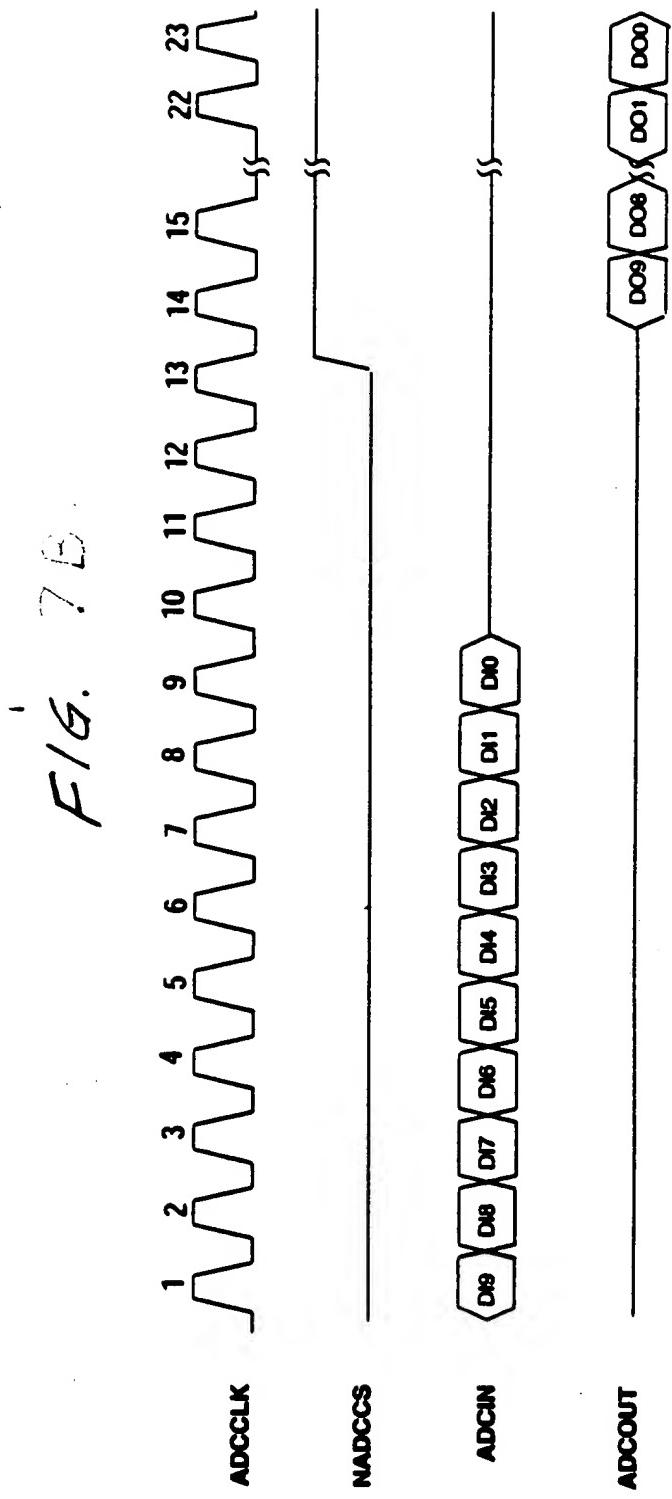


FIG. 8

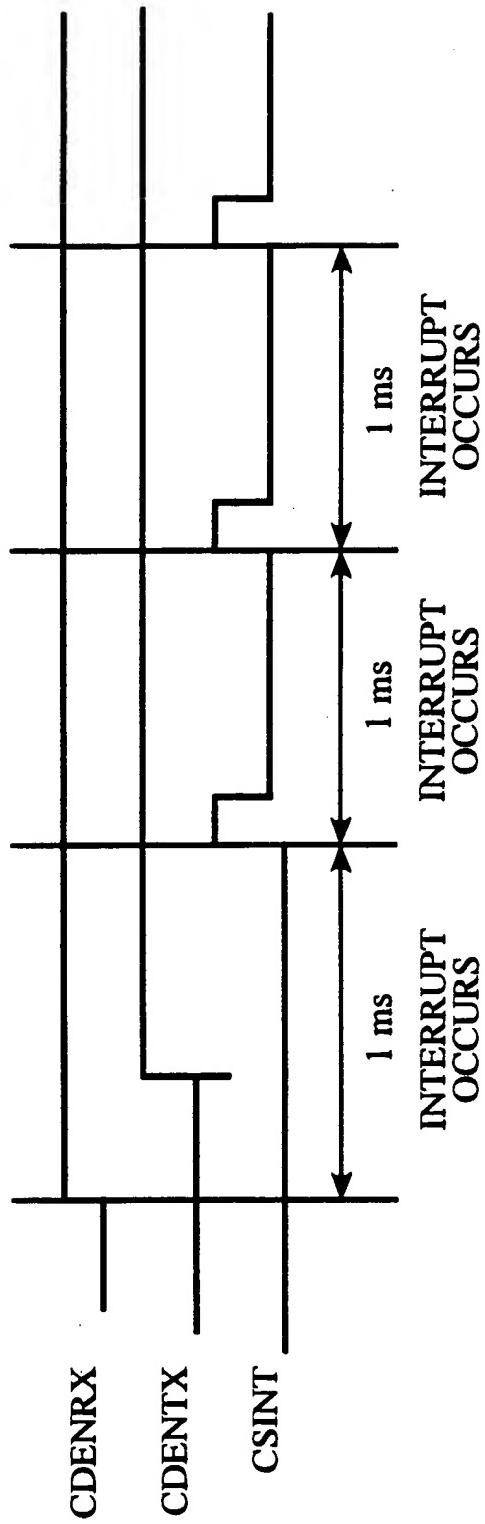
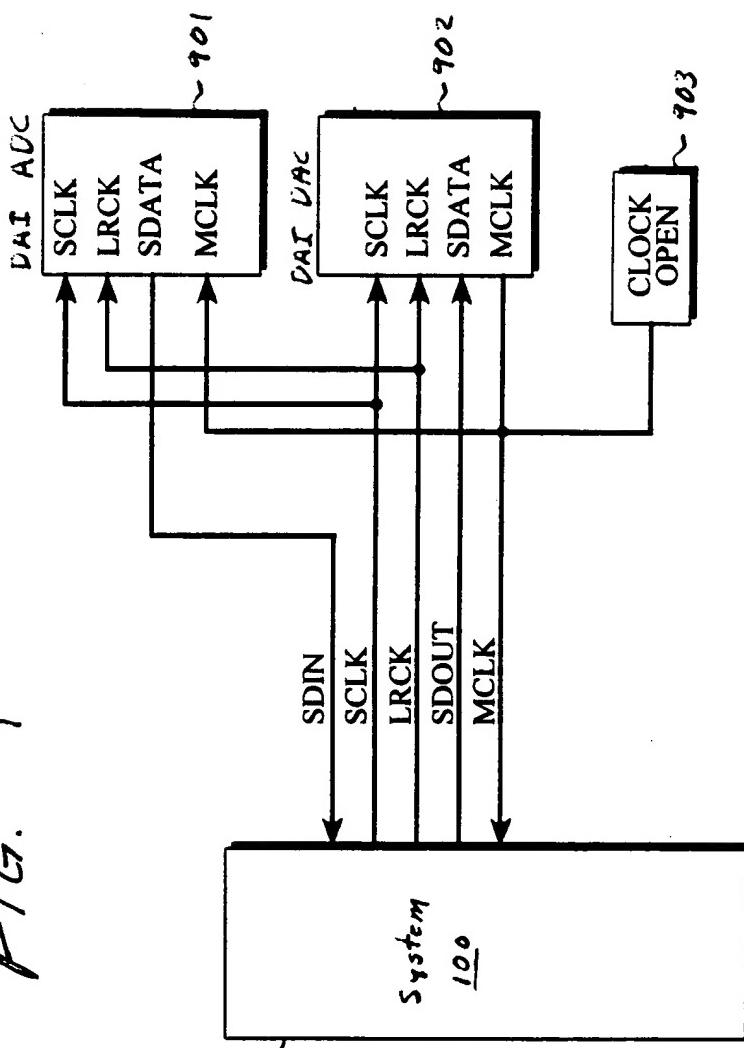


Fig. 9



中原書局影印

FIG. 10

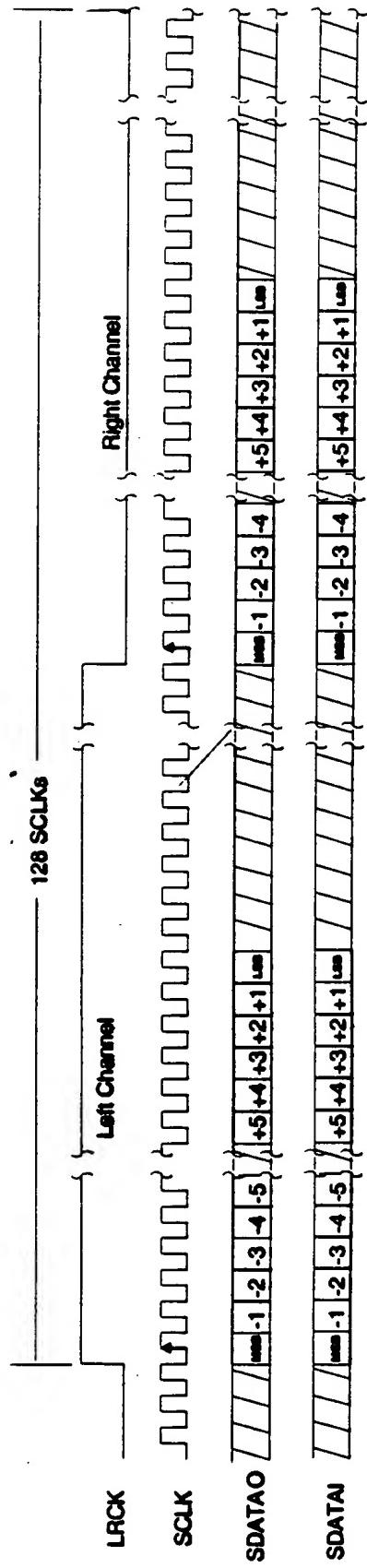
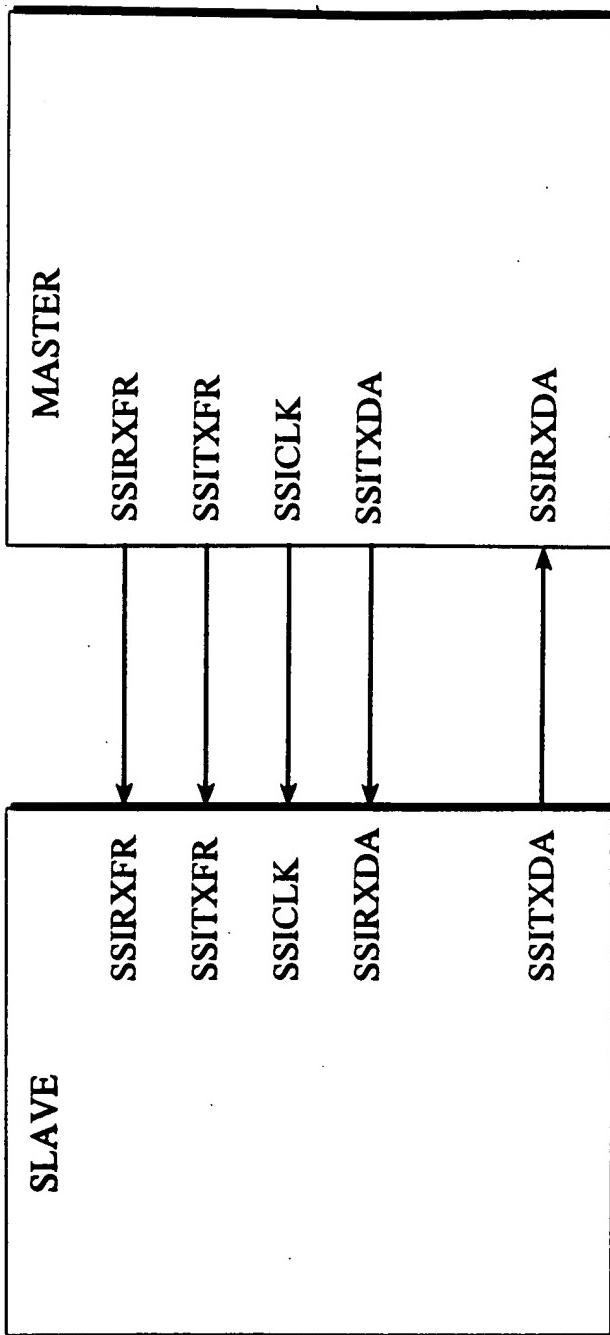


Fig. 11



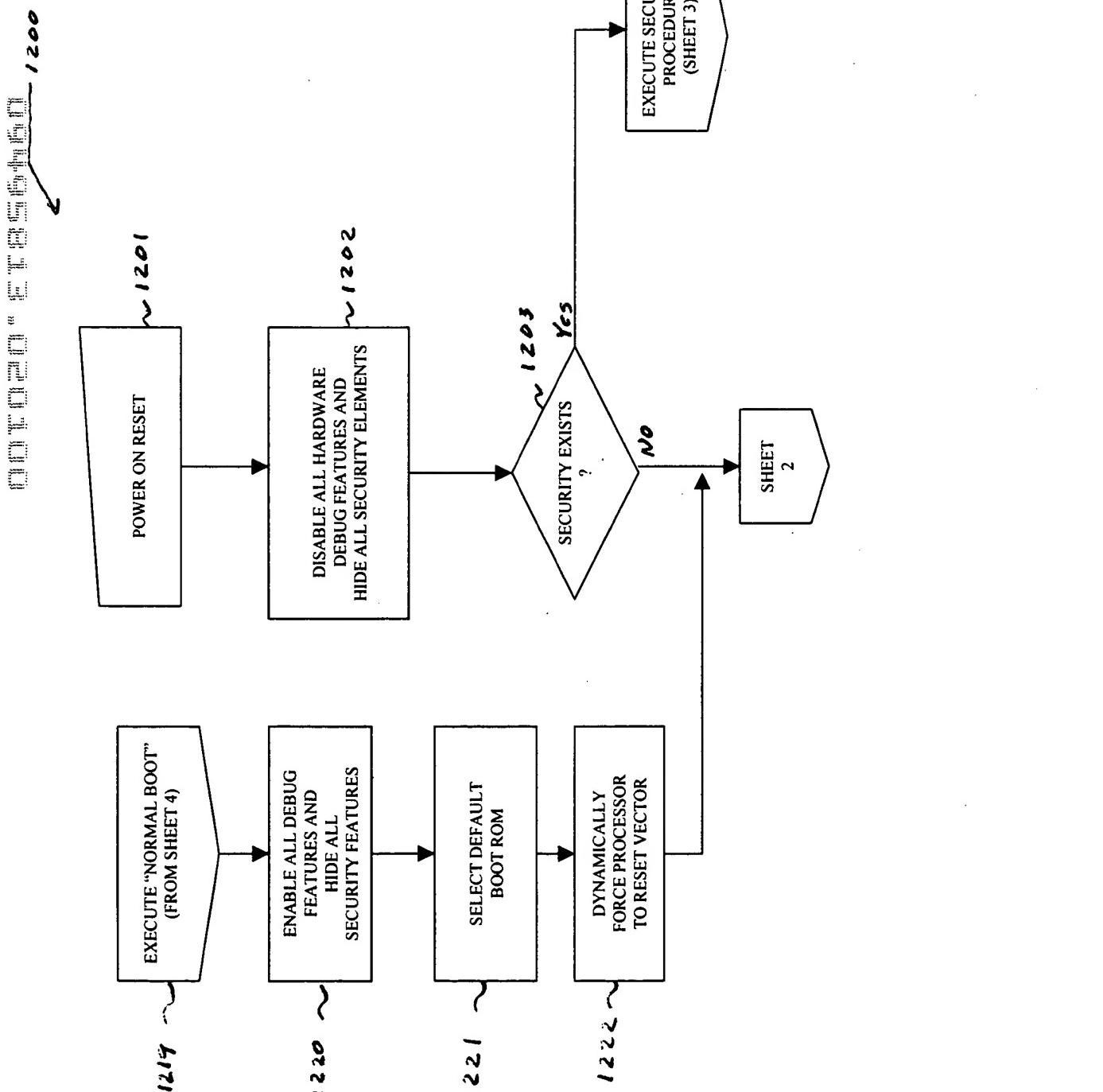
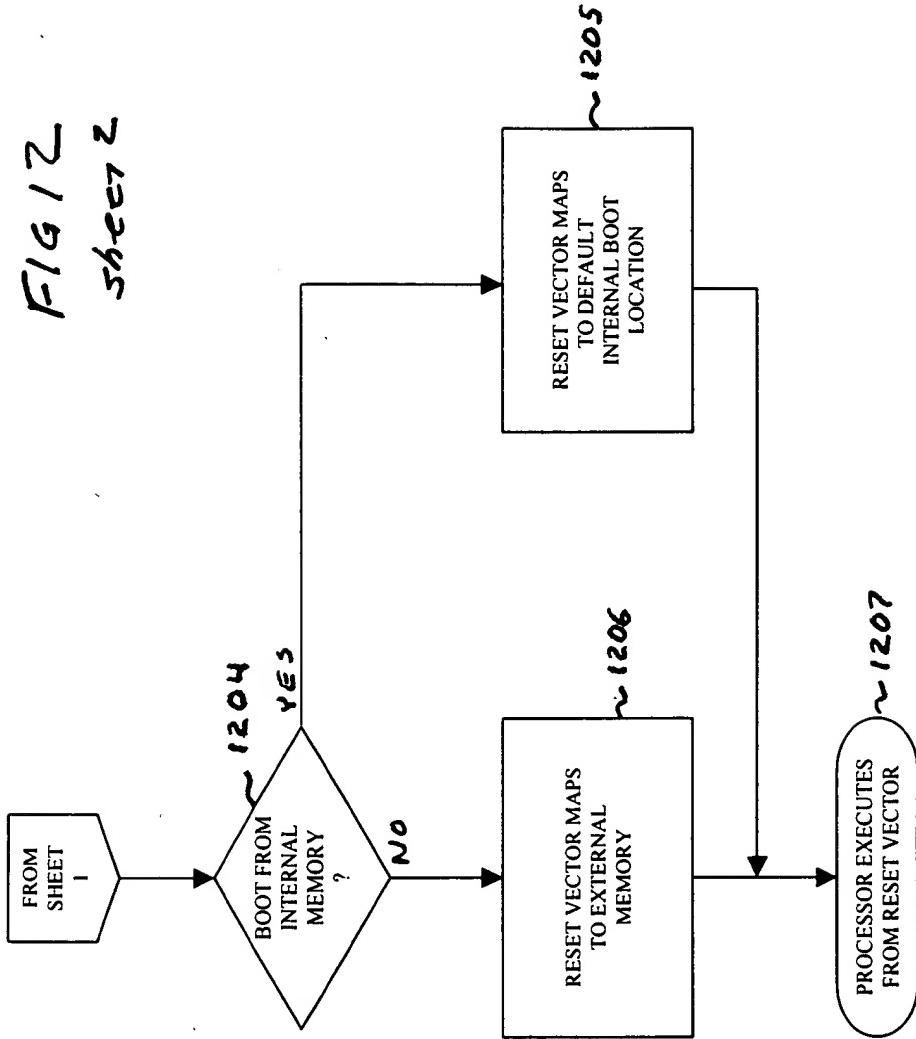
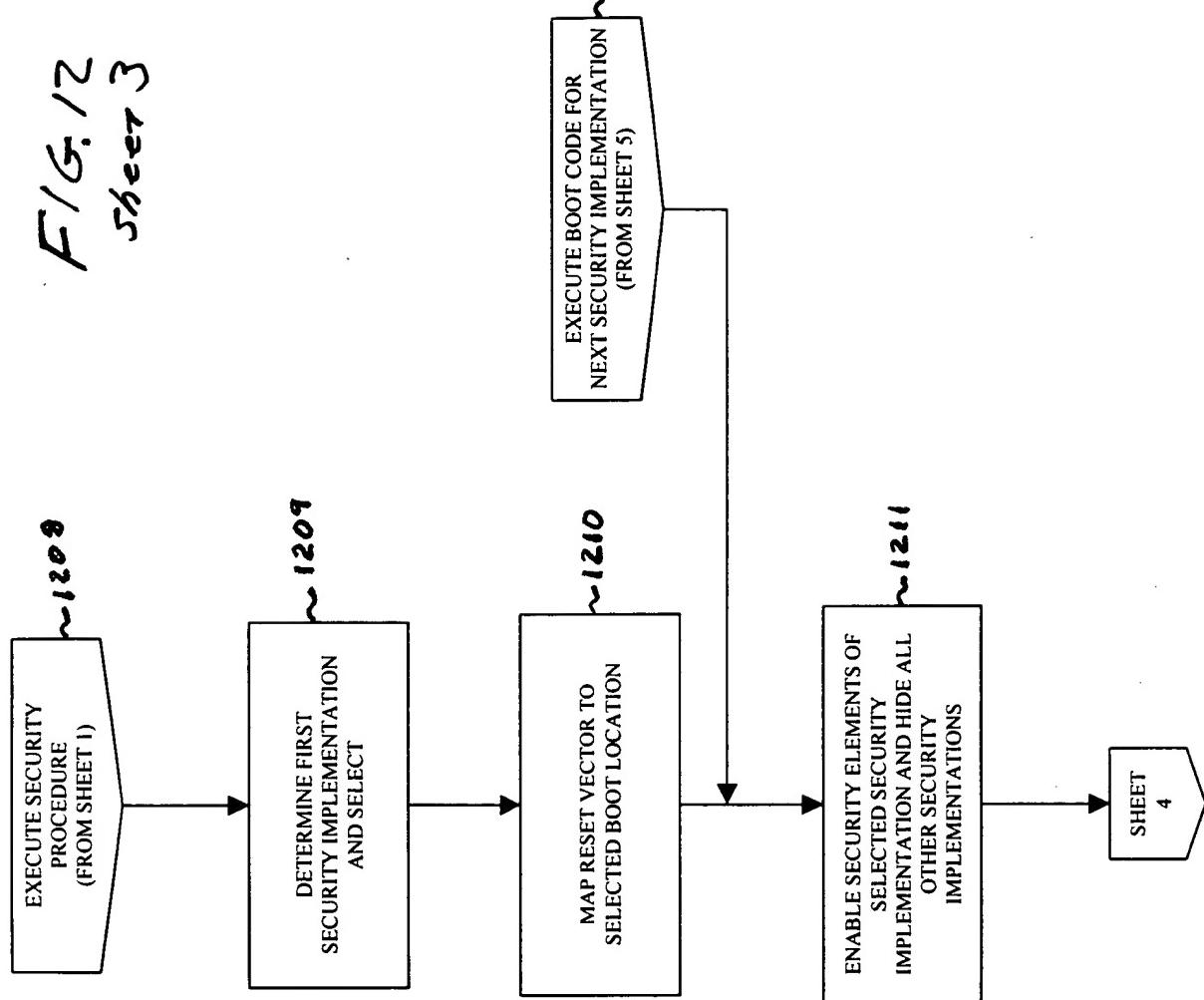


FIG 12
SHEET 2

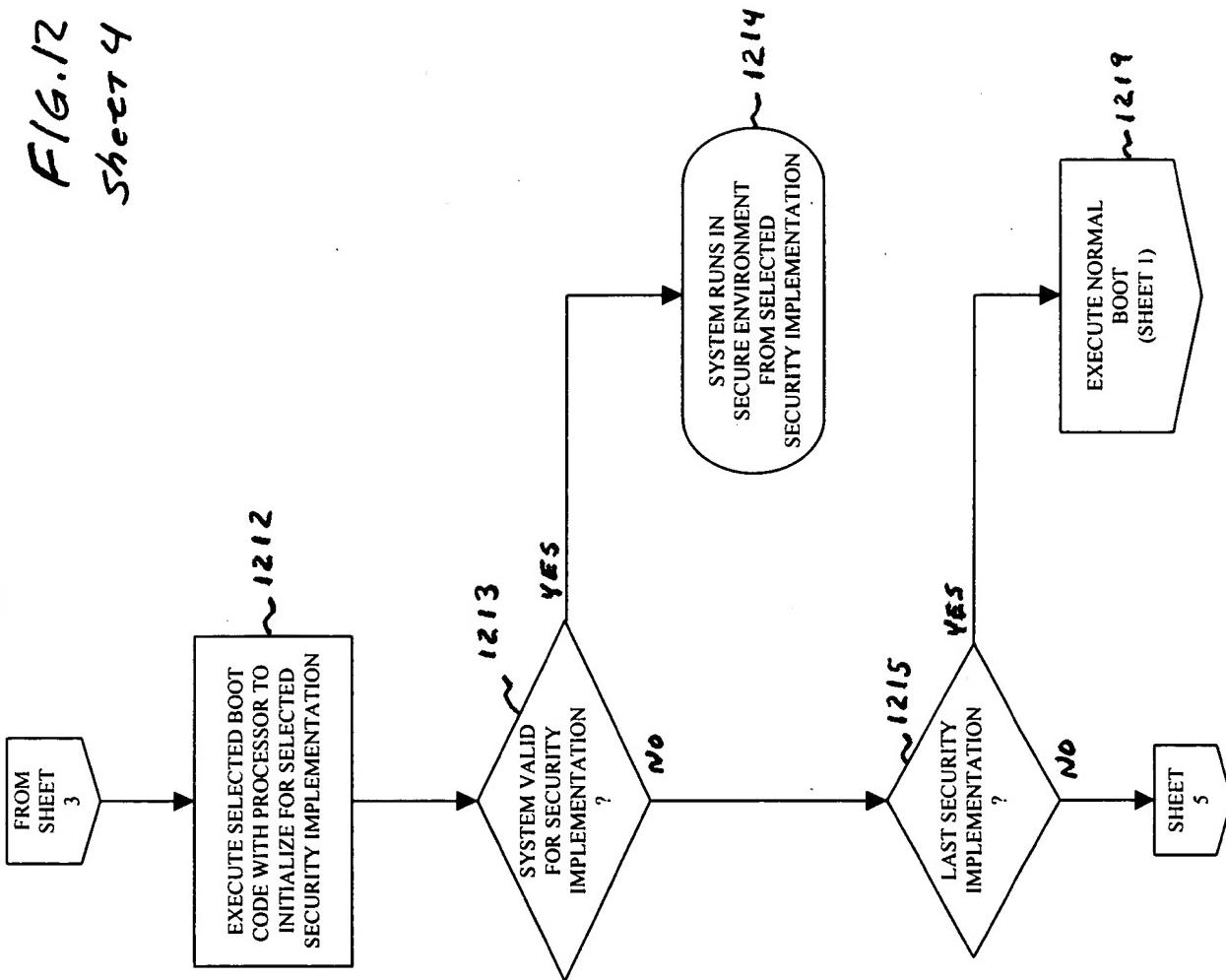


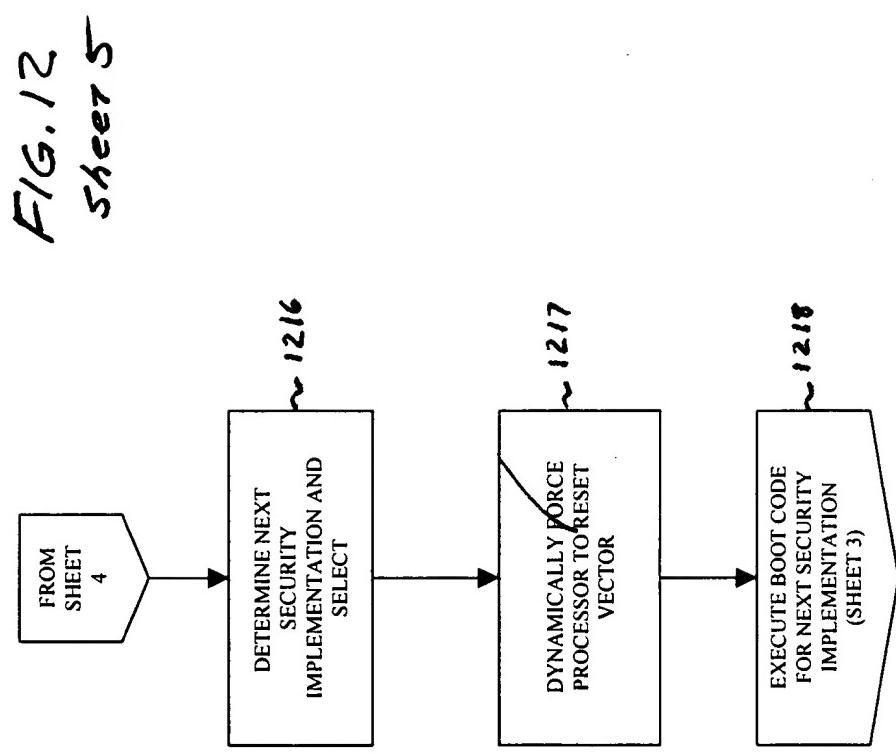
F/G. 12
SHEET 3



F16.12
Sheet 4

1210 1211 1212 1213 1214 1215 1216 1217 1218 1219





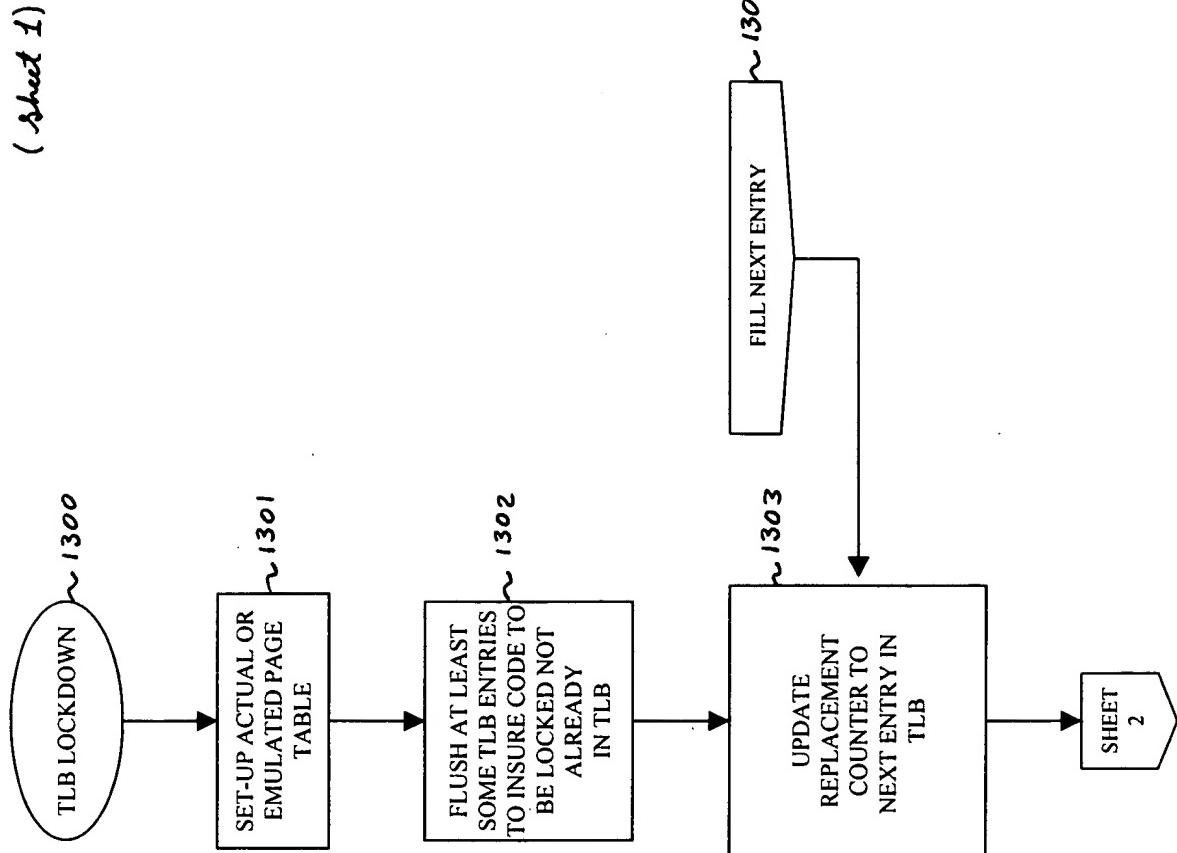
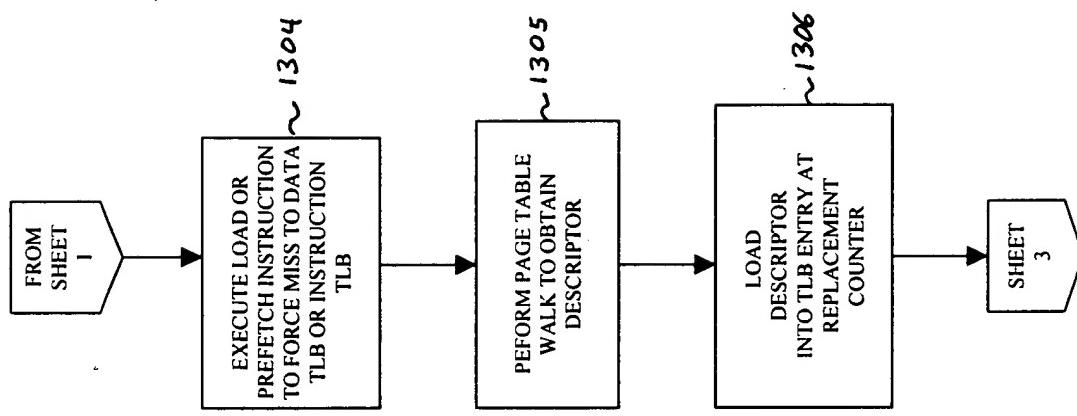


FIG. 13

(Sheet 2)



F16. 13

(Sheet 3)

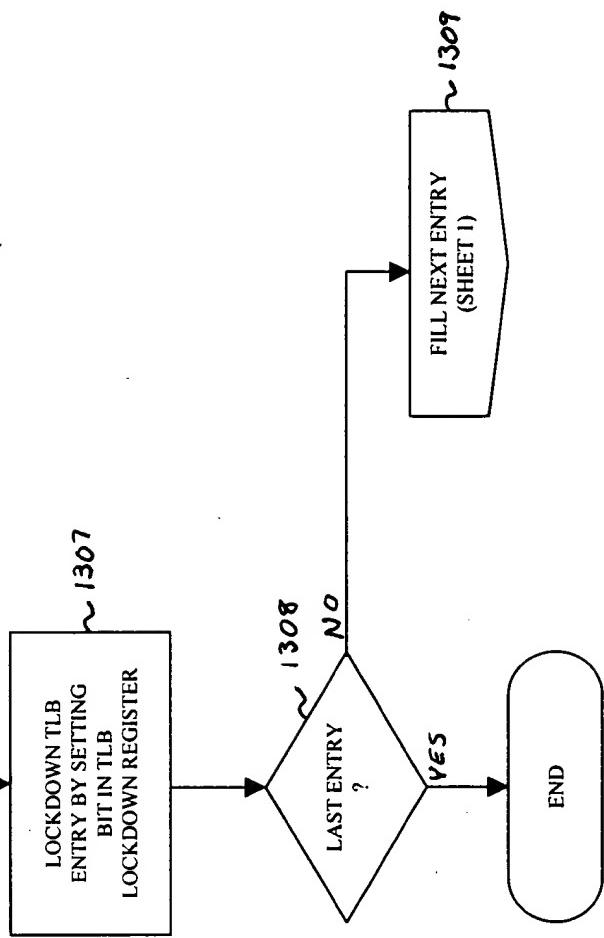


FIG. 14
(sheet 1)

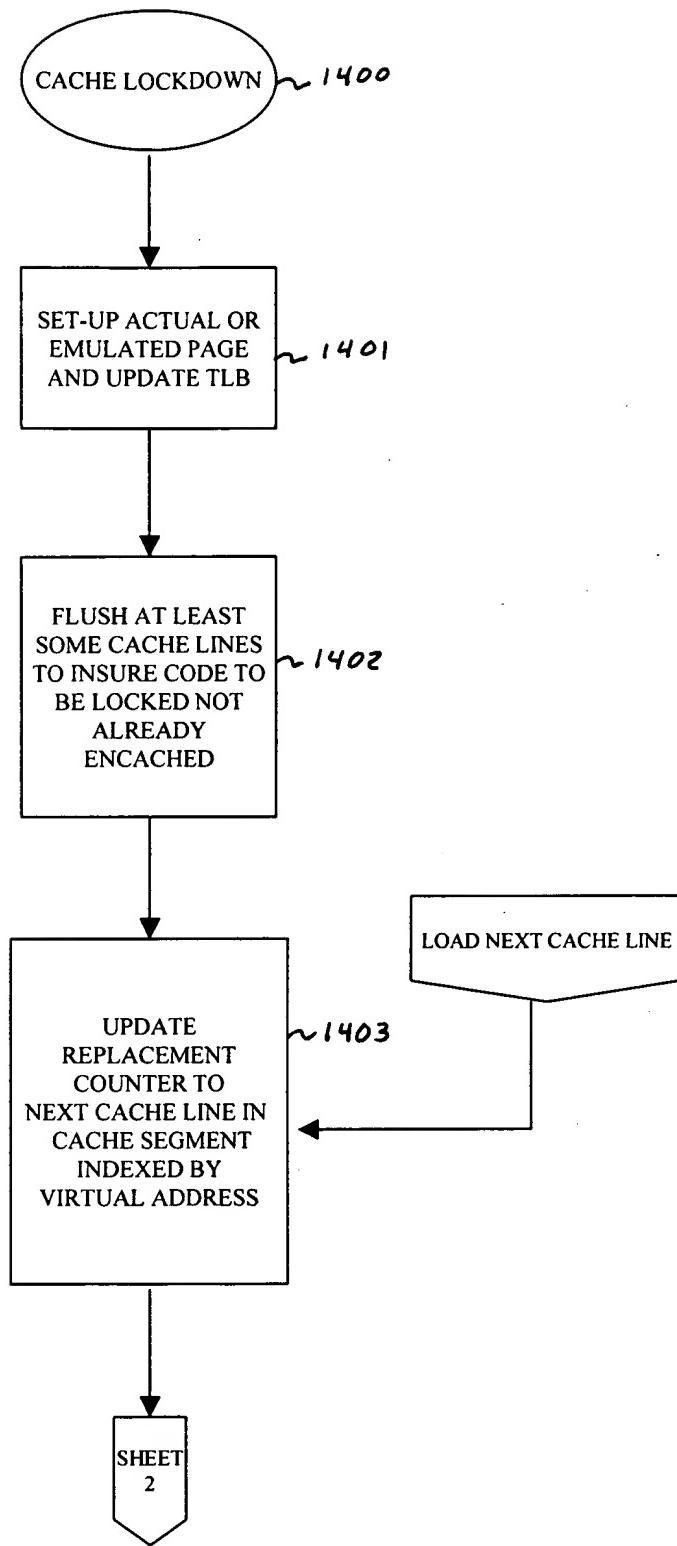
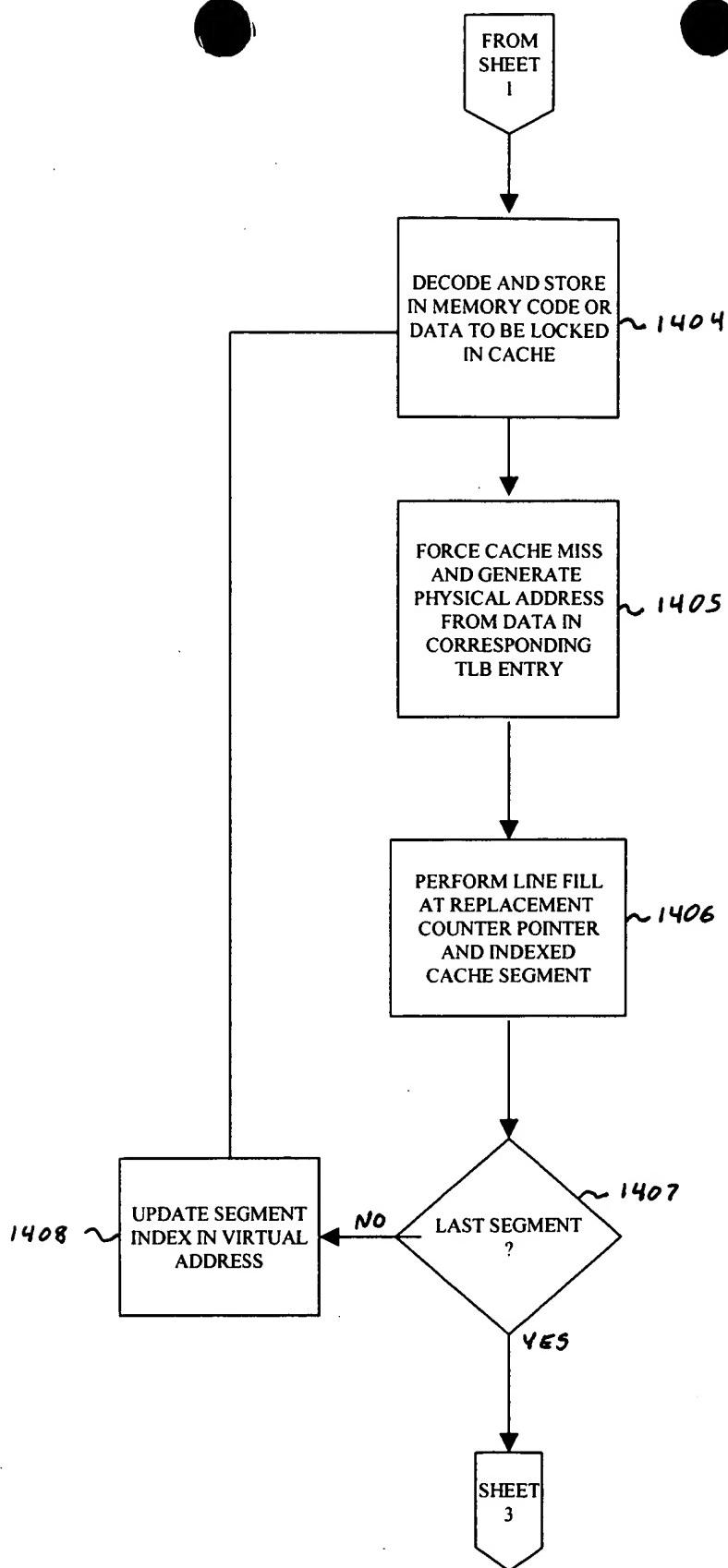


FIG. 14
(Sheet 2)



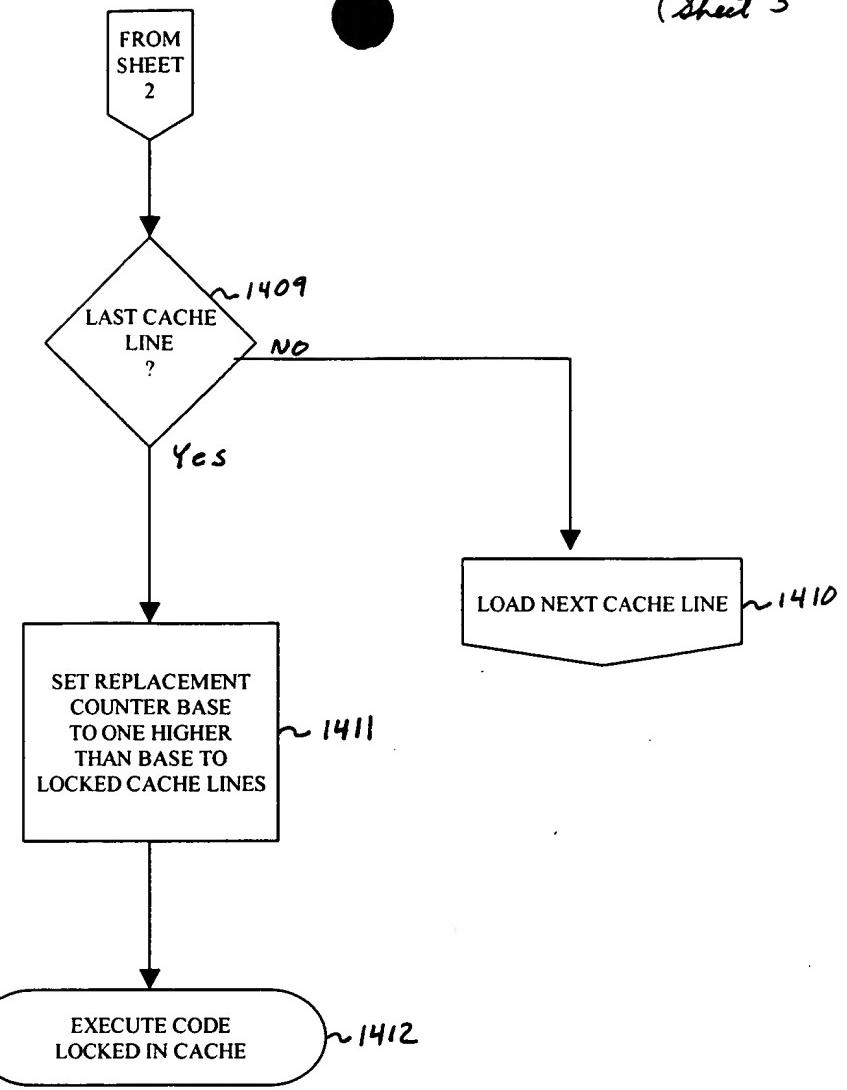


FIG. 15
(Sheet 1)

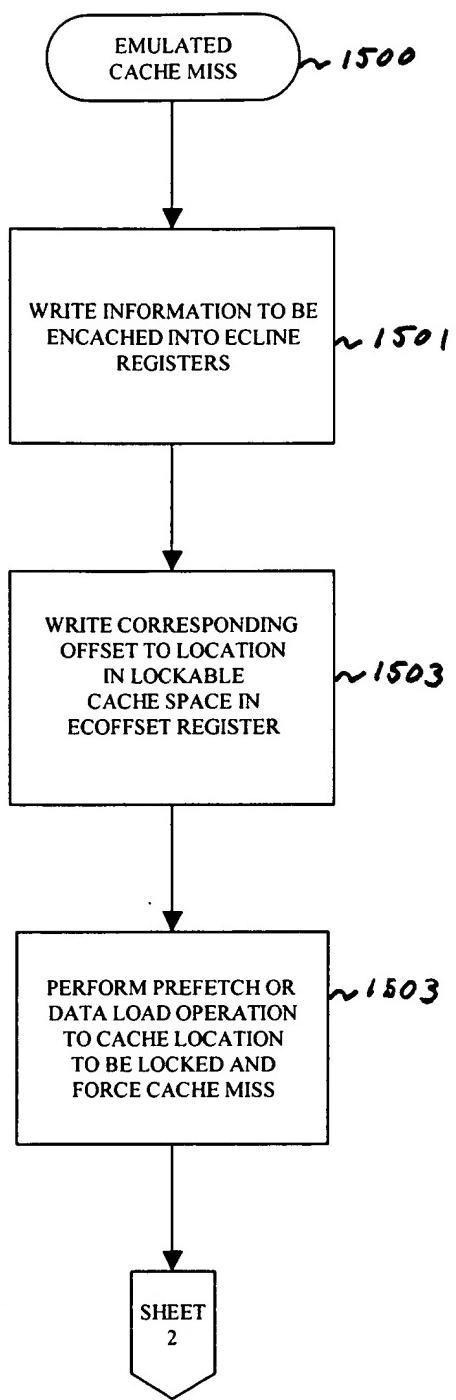


FIG. 15
(sheet 2)

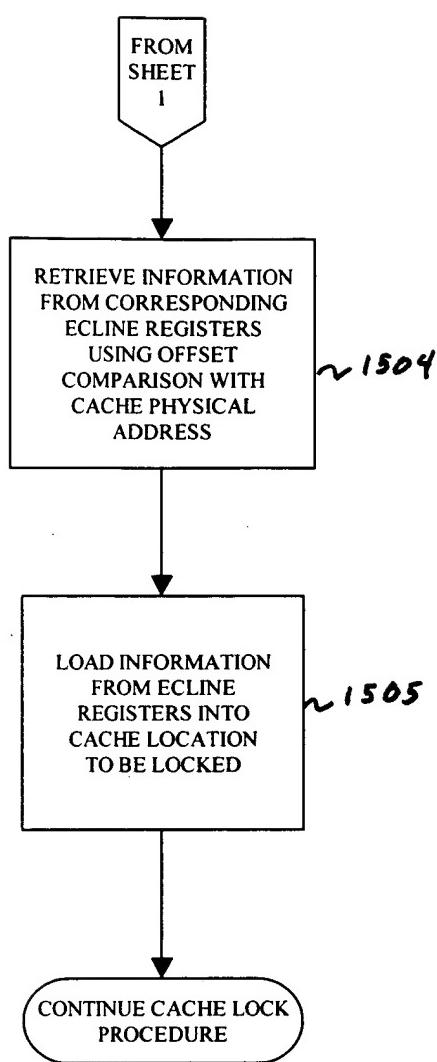


FIG. 16A
(Sheet 1)

00110010-00110010-0010

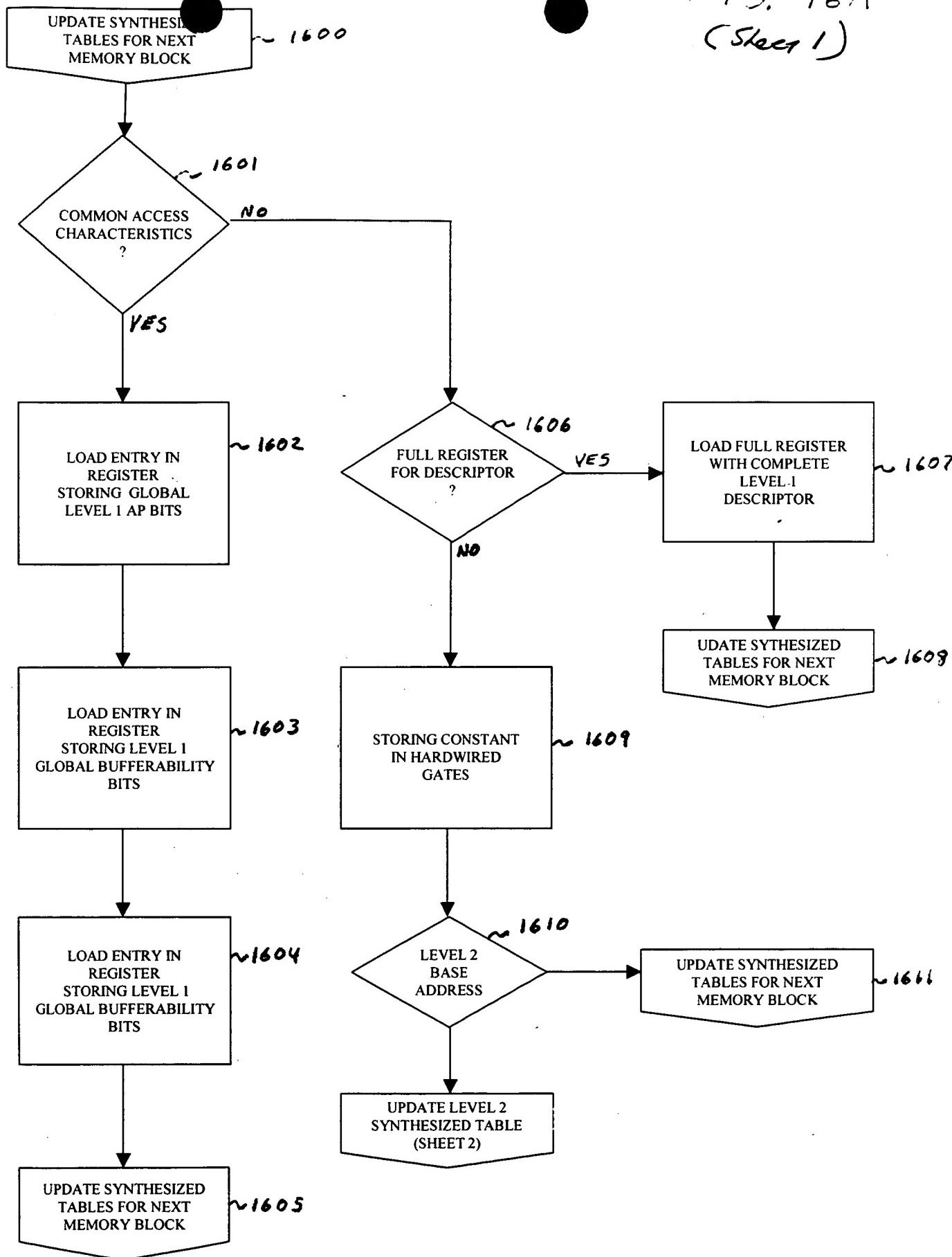


FIG. 16A
(Sheet 2)

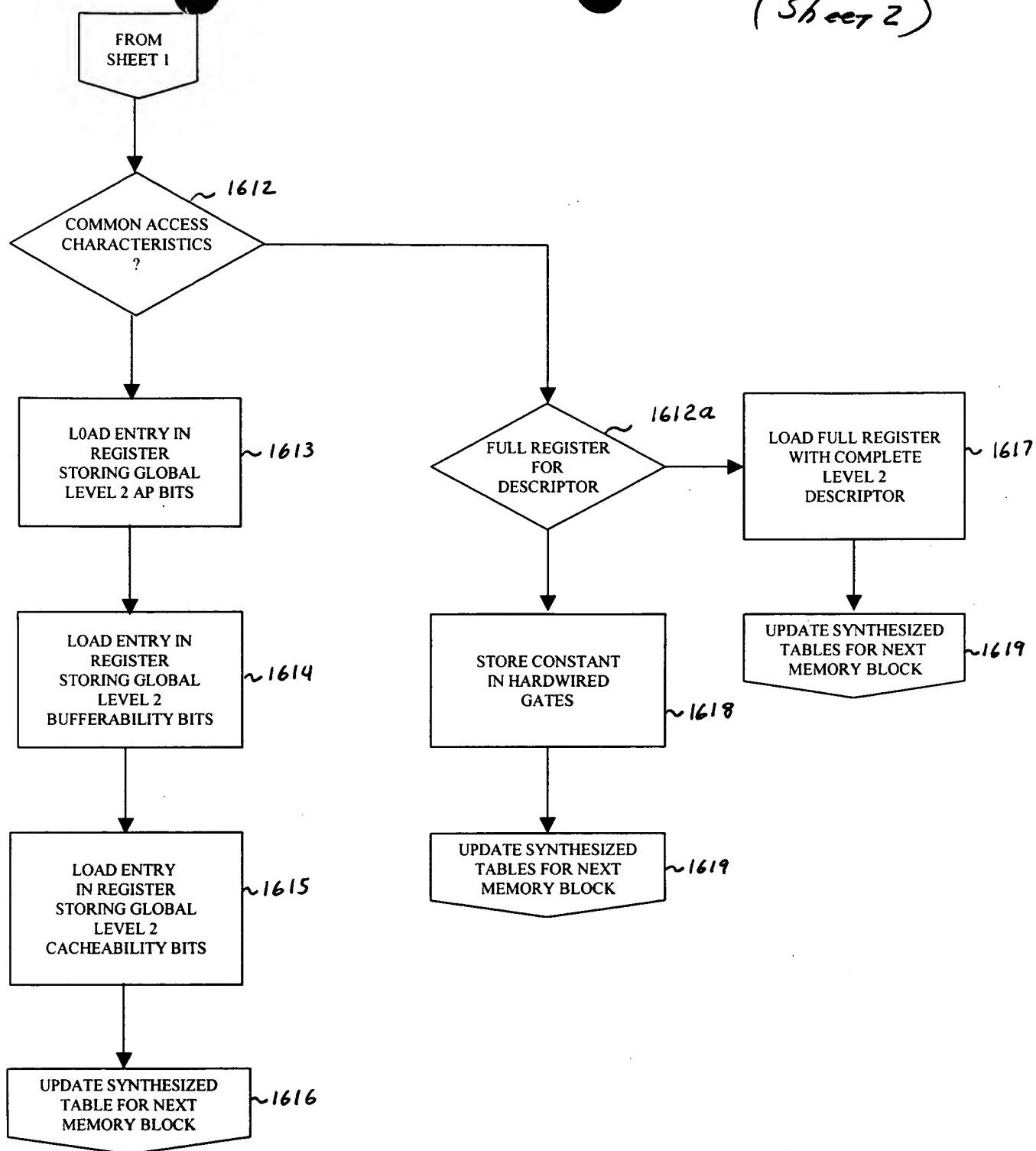


FIG. 16B
(SHEET 1)

CONTINUE ON SHEET 2

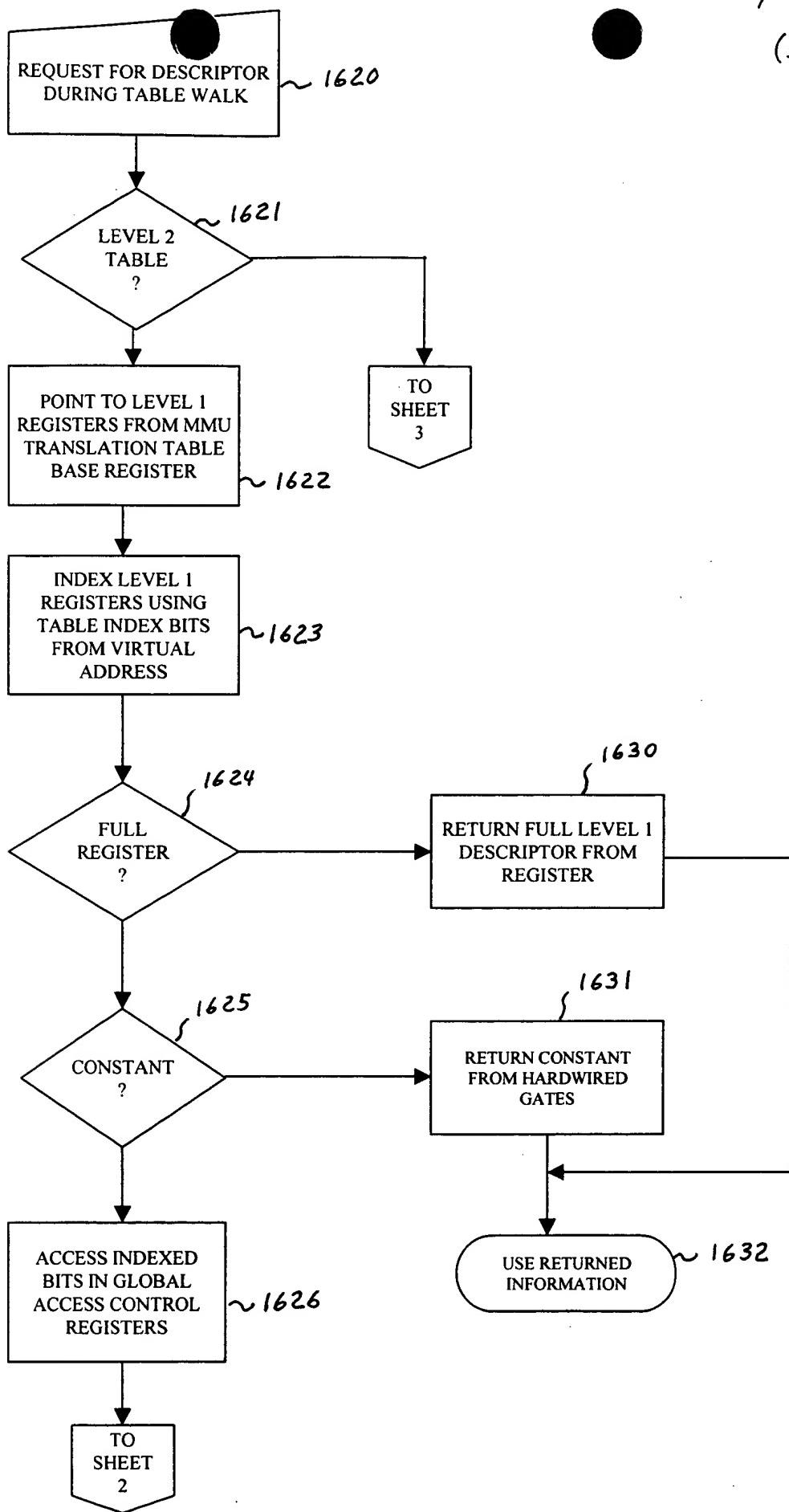


FIG. 16B
(SHEET 2)

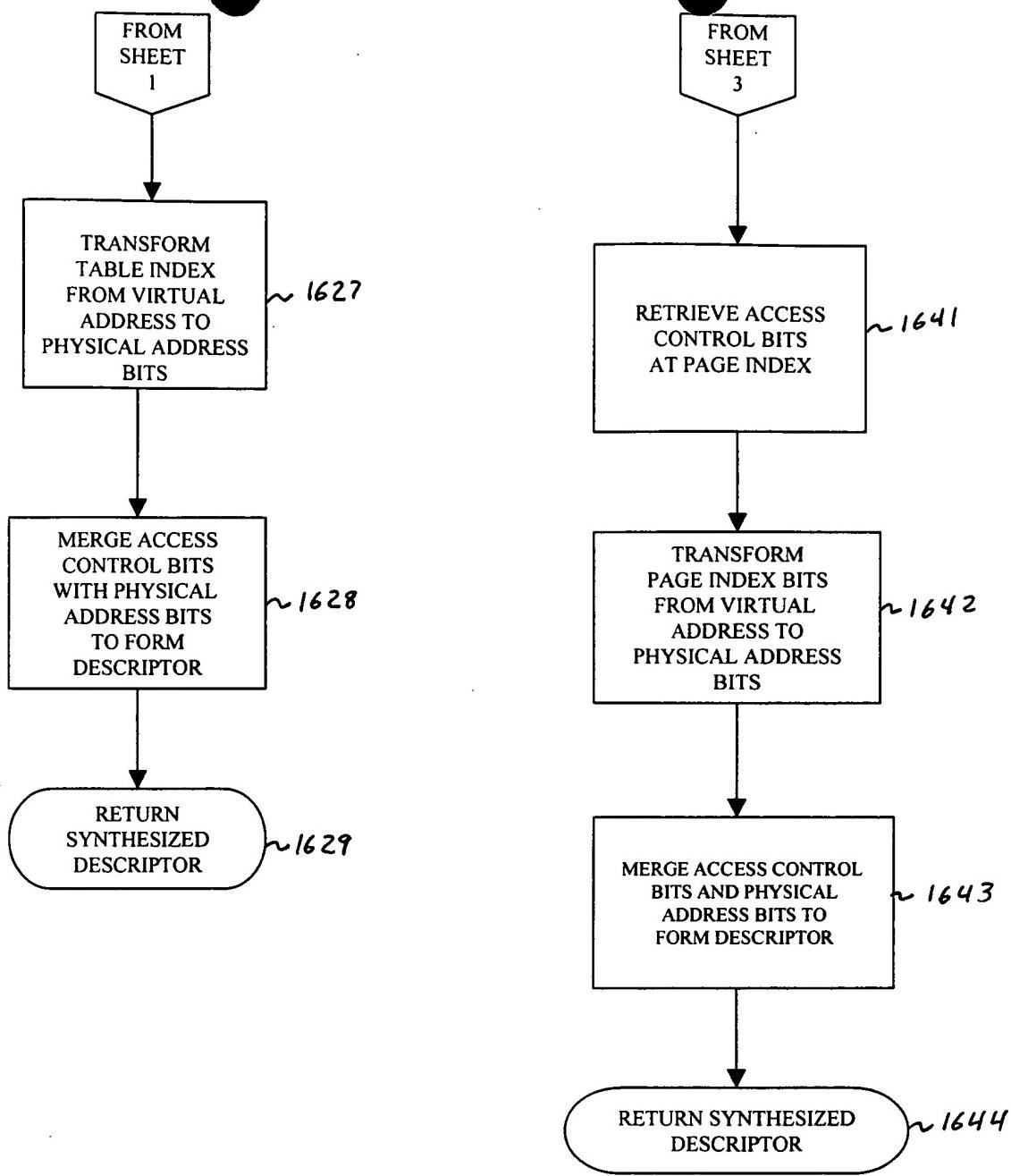
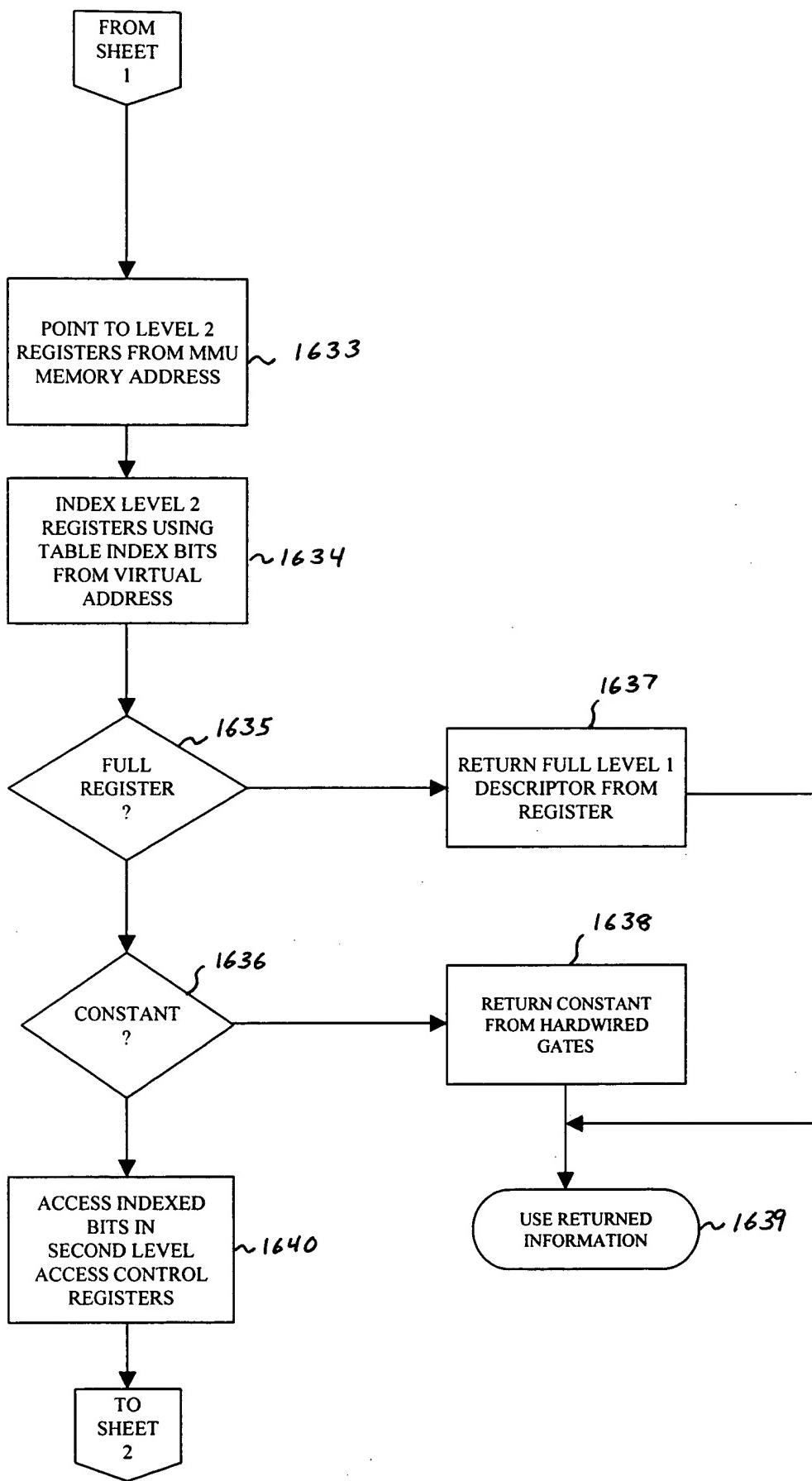


FIG. 16B
(SHEET 3)



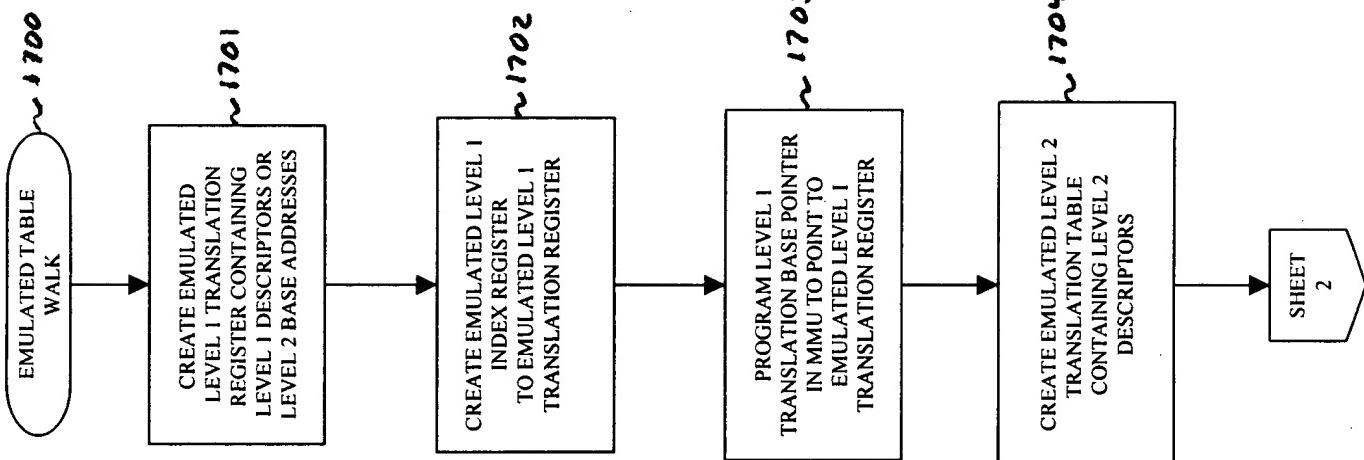
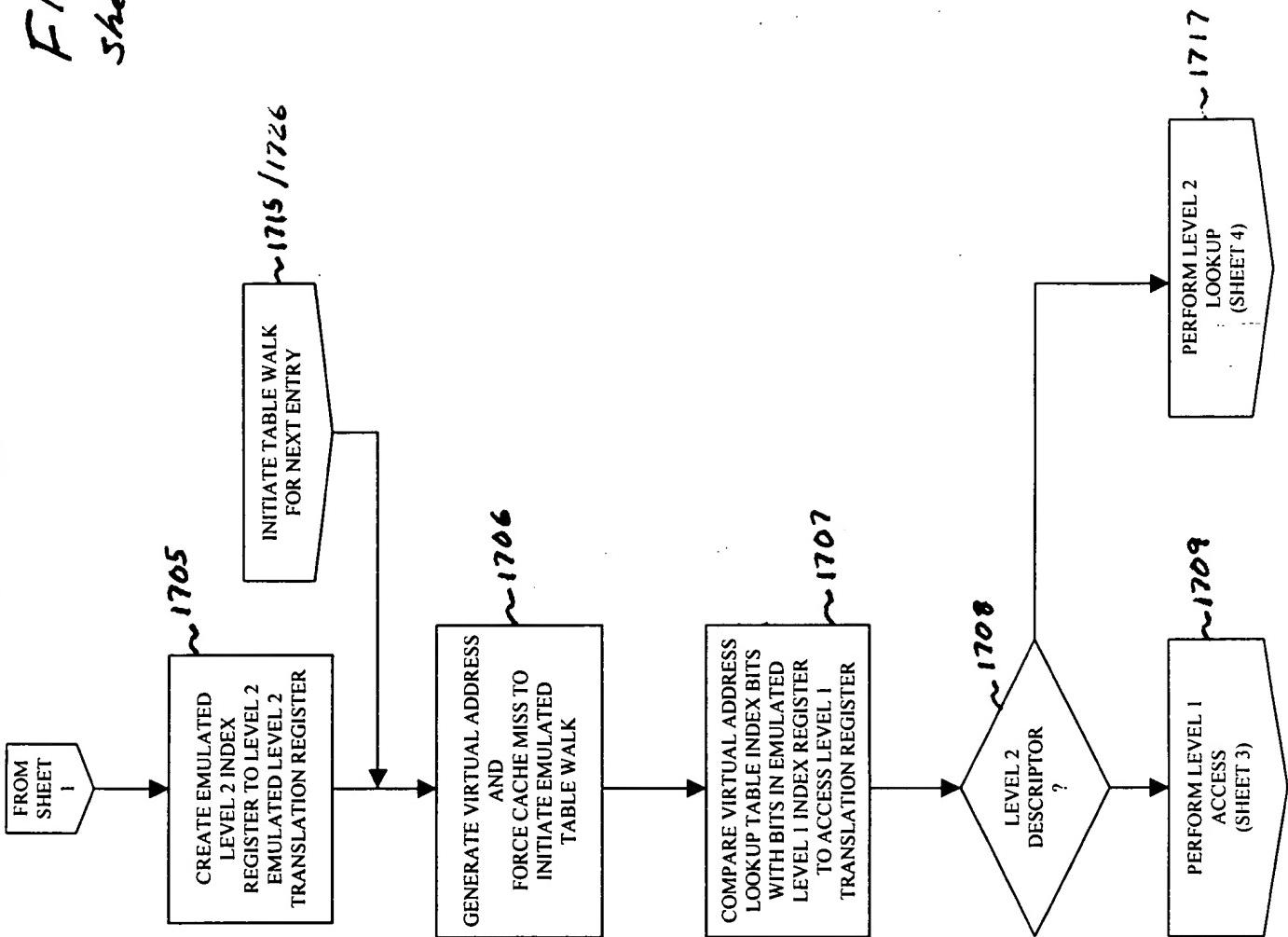


FIG. 17
Sheet 2

□ □ □ □ □ □ □ □ □ □ □



F1G.17
SHEET 3

FROM SHEET 2

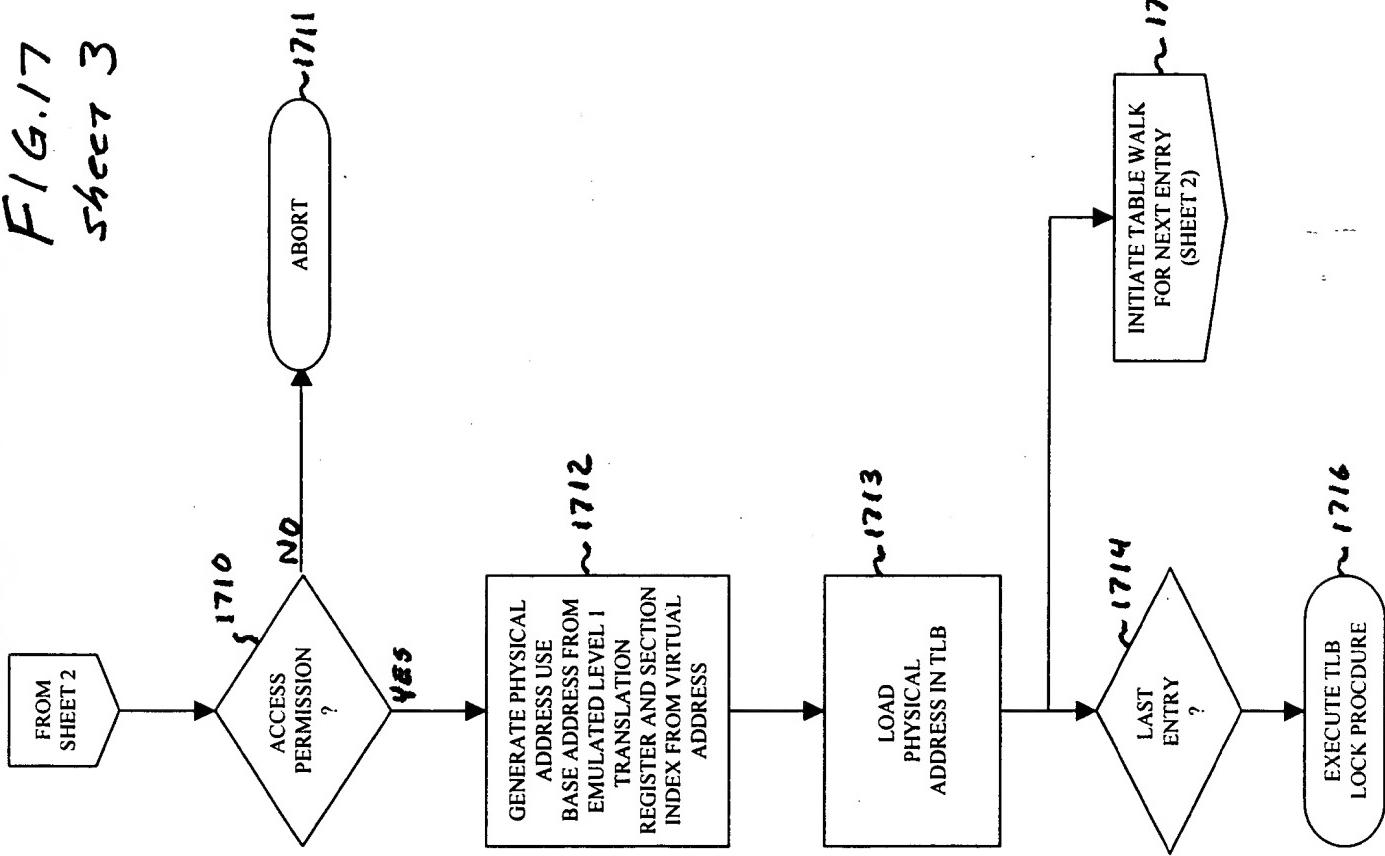
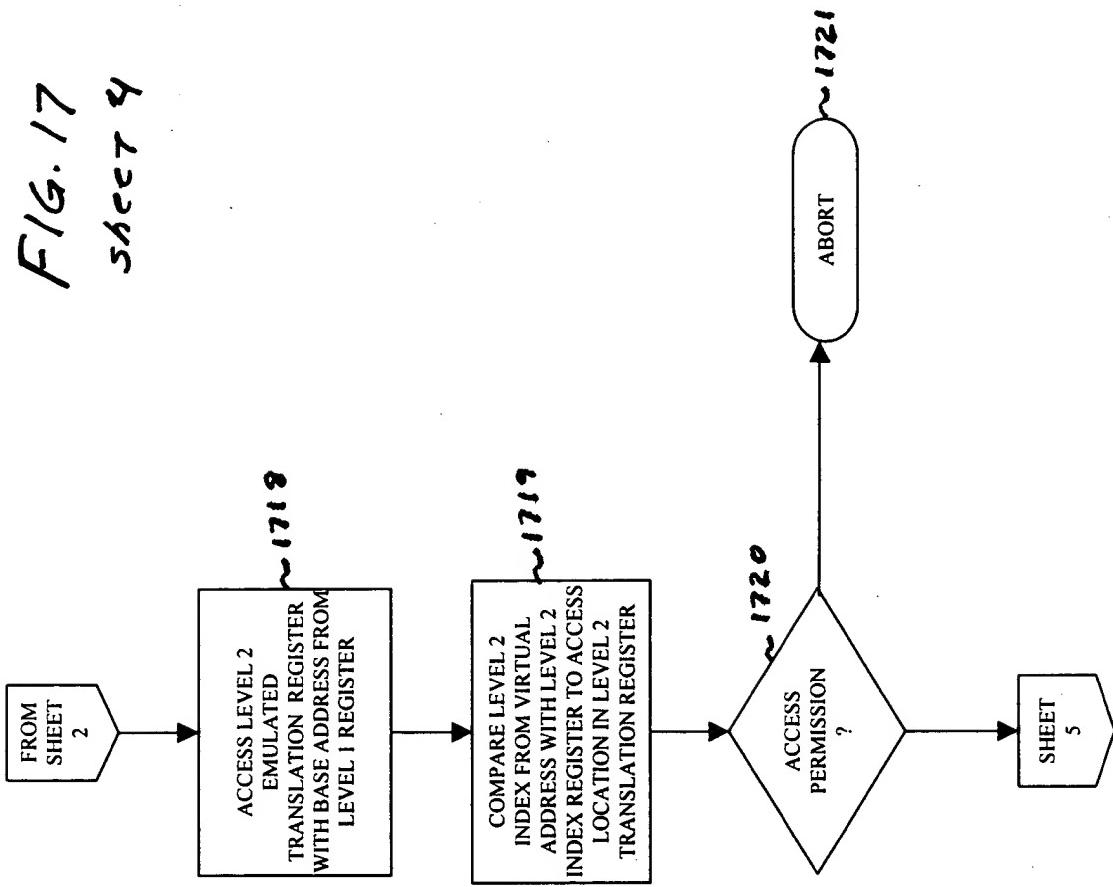


FIG. 17
SHEET 4



F1G. 17
Sheet 5

